

\sim T	•		

NOTIFICATION OF ELECTION

(PCT Rule 61.2)

	m the IN	TEDAL	ATION	AL"DI	
- -r	m the liv	PRNA	A I II IIV	AI RU	REALI

Commissioner **US Department of Commerce** United States Patent and Trademark Office, PCT 2011 South Clark Place Room CP2/5C24 Arlington, VA 22202 ETATS-UNIS D'AMERIQUE

in its capacity as elected Office

te of mailing (day/	
20 February 2	2001 (20.02.01)
· ·	

International application No. PCT/US00/12796

international filing date (day/month/year)

09 May 2000 (09.05.00)

Applicant's or agent's file reference 20631-11PC

Priority date (day/month/year) 11 May 1999 (11:05.99)

Applicant

	**************************************	11 Decem	ber 2000 (1	1:12.00) ""	Charles Comme			
		ا حام المانية المانية	International D					
in a notice e	ffecting later election	on tilea with the	nternational b	ureau on.		3.44% 3.44%		
				7				
			* * * * * * * * * * * * * * * * * * * *					To-sal
	า							
2. The election X	was	(0)	- 00					
Service of the servic	was not				ilian ildi	dans en e	Company	
made before the e	xpiration of 19 mon	the from the nric	rity date or w	hara Rula 32	annlies wi	hin the tin	ne limit u	nder
Rule 32.2(b).	· ·			11010 11010 02	. арриоз, чт			
		,	:				.	
		·						•
							• • • •	
•						•		7
				•		• • • • •		

The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20, Switzerland

Authorized officer

Antonia Muller

Telephone No.: (41-22) 338.83.38

Facsimile No.: (41-22) 740.14.35

PATENT COOPERATION TRE

PCT

REC'D 2 9 JAN 2002

MIPO

(703) 308-7790

PCT

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

INTERNATI	ONAL PRELIMINARY EX	CAMINATION REPORT	9
	(PCT Article 36 and	Rule 70) 10/009 4	52
Applicant's or agent's file reference 20631-11PC		See Notification of Transmittal of In Preliminary Examination Report (Form PCT/	
International application No.	International filing date (day/mor	nth/year) Priority date (day/month/year))
PCT/US00/12796	09 MAY 2000	11 MAY 1999	
International Patent Classification (IPC) IPC(7): G06F 3/00, 13/00; H05K 5/00		361/6S±, 686	
Applicant SOCKET COMMUNICATIONS, INC.			
Examining Authority and is 2. This REPORT consists of a This report is also accompleen amended and are the	transmitted to the applicant acc total of sheets. panied by ANNEXES, i.e., sheets to basis for this report and/or sheets on 607 of the Administrative Insti-	of the description, claims and/or drawings s containing rectifications made before this	which have
3. This report contains indication		o.	
I X Basis of the report II Priority III Non-establishment IV Lack of unity of V X Reasoned statement citations and explain VI Certain documents of VII Certain defects in the	nt of report with regard to novel invention t under Article 35(2) with regard nations supporting such statement	lty, inventive step or industrial applicab to novelty, inventive step or industrial app	· .
Date of submission of the demand	Data of	completion of this report	
11 DECEMBER 2000		ANUARY 2002	
Name and mailing address of the IPEA/ Commissioner of Patents and Tradens Box PCT Washington, D.C. 20231	arks .	AATI LE KOWITZ	

Form PCT/IPEA/+09 (cover sheet) (July 1998)*

Facsimile No. (703) 305-3230

International application No.

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

PCT/US00/1279	96

I. B	asis o	f the repo	ort			•	
1 Wirl	regar	d to the ele	ments of the interna	utional applicati	on:*		
\mathbf{x}			nal application as	• •	•	-	
=		description		01.6			
X		es					on originally filed
	nage	s	NONE				, as originally filed
		s			filed with the le	etter of	, filed with the demand
	1				,		
X	the o	claims:					
	-	s					, as originally filed
		s					tement) under Article 19
		s	NONE NONE	C:1 - 1	.'.11		, filed with the demand
	page	:s	NONE	, filed w	ith the letter of		,
\mathbf{x}	the c	drawings:					
LA)		s	1-24				, as originally filed
		s					, filed with the demand
	page	s	NONE		, filed with the lett	er of	
X			sting part of the	escription:			
		s					, as originally filed
	page	s	NONE				, filed with the demand
	page	'S	NONE		, filed with the letter	er of	
	the la	anguage o	f publication of	he internatio	the purposes of internation (underpurposes of internation	er Rule 48.3(b)).	der Rule 23.1(b)). ination (under Rules 55.2 and/
3. Wit	h rega	ard to any	nucleotide and/o	r amino acid out on the b	sequence disclosed in asis of the sequence	n the international a listing:	pplication, the international
	conta	ined in th	e international a	pplication in	printed form.		
					ion in computer read	lable form.	
Ħ			equently to this A		•		
H					computer readable fo	ırm	
님		•					4 4 4
	intern	iationai ap	plication as filed	has been furn	ished.		ond the disclosure in the
	The s been	tatement th furnished.	nat the information	recorded in c	omputer readable form	is identical to the w	vriten sequence listing has
4. X	The	amendmei	nts have resulted	in the cance	llation of:		
	X	the descr	ription, pages_	NONE			
	X		ns, Nos	NONE			
	\mathbf{x}		ings, sheets/fig	NONE			
5.	This		_	ome of) the ar	mendments had not bee	n made, since thev h	nave been considered to go
	beyo	and the disc	closure as filed, as	ndicated in the	e Supplemental Box (R	tule 70.2(c)).**	
in in	acemei	nt sheets wh ort as "ori	nich have been furn	shed to the red	ceiving Office in respons	se to an invitation und	der Article 14 are referred to n amendments (Rules 70.16
			et containing such	amendments	must be referred to ur	nder item 1 and ann	nexed to this report.

International application No.

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

PCT/US00/12796

V.	 V. Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement 					
1.	statement		-			
	Novelty (N)	Claims	NONE	YES		
		Claims	1-62	NO		
	Inventive Step (IS)	Claims	NONE	YES		
		Claims	1-62	_ NO		
	Industrial Applicability (IA)	Claims	1-62	YES		
		Claims	NONE	NO		
				_		

2. citations and explanations (Rule 70.7)

Claims 1, 28-30, 39, 40, and 56-58 lack novelty under PCT Article 33(2) as being anticipated by Kaneda et al., 5,18+,282 (hereinafter Kaneda).

- a. As to claims 1, 28, 30, and 40, Kaneda discloses a removable expansion card (note Figure 9) for a portable host, comprising an expansion card frame and PCB, a host-interconnect for coupling with the host, an I/O interconnect for coupling with the external I/O device (note Figure 9, element 27), I/O adapter circuitry for the I/O device (inherent in the serial to parallel conversion required to interface a telephone device with a computer), a slot for a removable memory (note Figure 9, elements 16 and 17 and column 5, lines 35-40), and removable memory adapter circuitry for the removable memory (inherent in the fact that the removable memory and host computer communicate) (note abstract, column 1, lines 12-25, and column 2, line 45 column 3, line 4).
- b. As to claim 29, Kaneda discloses that the removable memory is a private memory for application-specific circuitry and that the management of the removable memory is an ancillary function to the primary function of the specific application (note column 1, lines 12-34).
- c. As to claims 39, 56, 57, and 58, Kaneda discloses a slot assembly for a removable expansion memory comprising a PCB, an I/O connector mounted on the PCB providing a first partial bottom of slot, a guide/connector assembly mounted on PCB having connector fingers and providing a second partial bottom of the slot, rear sides of the slot, and slot back stop, upper outside frame of expansion module frame providing front sides of slot and the lid of the expansion module providing the top of the slot (note Figures 6 and 9).

Claims 2, 3, 5, 7, 9-17, 41, 42 lack an inventive step under PCT Article 33(3) as being obvious over Kaneda et al., 5,184,282 (hereinafter Kaneda).

a. As to claim 2, Kaneda fails to disclose that the card is a CompactFlash card.

Examiner takes Official Notice that CompactFlash cards are well known in the art of removable expansion cards. (Continued on Supplemental Sheet.)

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

Supplemental Box

(To be used when the space in any of the preceding boxes is not sufficient)

Continuation of: Boxes I - VIII

Sheet 10

V. 2. REASONED STATEMENTS - CITATIONS AND EXPLANATIONS (Continued):

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a CompactFlash in the system of Kaneda so as to allow the system to be compatible with a widely used standard and to allow the system to take advantage of the many benefits provided by a CompactFlash card.

b. As to claim 3, Kaneda fails to discloses that the removable memory slot is compatible with a MMC, and the removable memory adapter circuitry is MMC adapter circuitry.

Examiner takes Official Notice that MMC is well known in the art of removable expansion cards.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of MMC components and circuitry in the system of Kaneda so as to allow the system to be compatible with a widely used standard and to allow the system to take advantage of the many benefits provided by MMC.

c. As to claim 5, Kaneda fails to disclose that the I/O adapter circuitry is a LAN adapter and the I/O interconnect includes a cable having a standard LAN connector.

Examiner takes Official Notice that LAN cards, LAN cables and LAN connectors are well known in the art of networks.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a LAN card, LAN cable and LAN connector in the system of Kaneda so as to allow the system of Kaneda to function in a network environment.

d. As to claim 7, Kaneda fails to disclose that the I/O interconnect is a Honda-style 15-pin connector integral to the card.

Examiner takes Official Notice that Honda-style 15-pin connectors are well known in the art of expansion cards.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of Honda-style 15-pin connectors in the system of Kaneda so as to be compatible with a widely used connector and take advantage of the benefits provided by Honda-style 15-pin connectors.

e. As to claims 9-17, Kaneda discloses some of the claim limitations, as discussed above with respect to claims 1-8, but fails to disclose the environmental limitations of video and audio digitally encoded data.

Examiner takes Official Notice that the processing of video and audio data in the manner recited in the claim limitations is well known in the art of multimedia processing.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of any kind of digitally encoded data, including digitally encoded video and audio data, as the data stored in the removable memory since the type of data stored and processed would not change the overall configuration of an expansion card with a slot for a removable memory and a connector for an I/O device.

f. As to claim 41, the claimed elements have already been discussed above with respect to claim 1 above, with the exception of a second-level I/O interface for coupling with a second external I/O device, and second-level I/O adapter circuitry for the second I/O device.

Having a second-level I/O interface for coupling with a second external I/O device, and second-level I/O adapter circuitry for the second I/O device is simply a duplication of the first I/O interface, external I/O device and I/O adapter. And according to In re Harza, 274 F.2d 669, 124 USPQ 378 (CCPA 1960), mere duplication of parts has no patentable significance unless a new and unexpected result is produced, so that it would have been obvious to one of ordinary skill in the art at the time of the invention to have a second-level I/O interface for coupling with a second external I/O device, and second-level I/O adapter circuitry for the second I/O device.

g. As to claim 42, Kaneda discloses that the first external I/O device is a phone (note column 4, lines 60-62).

Claims 4 and 6 lack an inventive step under PCT Article 33(3) as being obvious over Kaneda et al., 5,184,282 (hereinafter Kaneda) in view of Corder, 5,615,344.

a. As to claim 4, Kaneda fails to disclose that the I/O adapter circuitry is a serial I/O adapter and the I/O interconnect includes a cable having a standard serial connector.

Corder discloses a card with a serial I/O adapter and an I/O interconnect that includes a cable having a serial connector (note Figures 1 and 3).

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a serial I/O adapter and serial connector so as to interface with the wide variety of serial I/O devices available in the art.

b. As to claim 6, Kaneda fails to disclose that the I/O adapter circuitry is a parallel adapter and the I/O interconnect includes a cable having a standard parallel connector.

Corder discloses a card with a parallel I/O adapter and an I/O interconnect that includes a cable having a parallel connector (note Figure 1).

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a serial I/O adapter and serial connector so as to interface with the wide variety of parallel I/O devices available in the art.

Claims 8 and 43-48 lack an inventive step under PCT Article 33(3) as being obvious over Kaneda et al., 5,184,282



INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No.

PCT/US00/12796

Supplemental Box

(To be used when the space in any of the preceding boxes is not sufficient)

Continuation of: Boxes I - VIII

Sheet 11

(hereinafter Kaneda) in view of Krishan et al., 5,611,055 (hereinafter Krishan).

a. As to claim 8, Kaneda fails to disclose that the card is designed to abut and fasten with at least part of the I/O device such that the I/O interconnect for coupling with the I/O device is cableless.

Krishan discloses that a card is designed to abut and fasten with at least part of the I/O device such that the I/O interconnect for coupling with the I/O device is cableless.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a card that is designed to abut and fasten with at least part of the I/O device such that the I/O interconnect for coupling with the I/O device is cableless in the system of Kaneda, as Krishan teaches, so as to avoid problems associated with cables, such as tangling with other cables and cluttering and not having cables of sufficient length.

b. As to claim 43, Kaneda fails to disclose that the second-level I/O adapter circuitry includes wireless interace circuitry, the second-level I/O interface includes a wireless transducer, and the second-level I/O device includes wireless network interface circuitry, and the coupling to the second external I/O device is wireless, but does disclose that the connector 27 allows communication devices such as networks to be coupled to card.

Krishan discloses that the I/O interconnect for coupling with the I/O device is cableless.

It would have been obvious to one of ordinary skill in the art at the time of the invention to couple a wireless network, with its associated transducer, to the card to allow a network to be coupled to the system without dealing with problems associated with cables, such as tangling with other cables and cluttering and not having cables of sufficient length.

c. As to claim 44, Kaneda fails to disclose that the first wireless network interface circuitry is RF wireless network interface circuitry and the wireless transducer includes an antenna.

Examiner takes Official Notice that RF wireless network interface circuitry and antennas are well known in the art of wireless communications.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of RF wireless network interface circuitry and antennas in the system of Kaneda, since RF wireless network interface circuitry and antennas are reliable and cost-effective means for carrying out communication over a wireless network.

d. As to claim 45, Kaneda fails to disclose that the RF wireless network interface circuitry is compatible with the Bluetooth wireless network standard.

The Bluetooth wireless network standard is a well known standard in the art of wireless communications.

It would have been obvious to one of ordinary skill in the art at the time of the invention to have the RF wireless network interface circuitry be compatible with the Bluetooth wireless network standard so as to allow the RF wireless network interface circuitry to be in conformance with Bluetooth, which is among the latest developments in wireless communications, thereby allowing the system of Kaneda to be implemented in the many systems which use the Bluetooth standard.

Claims 18-27 and 31-38 lack an inventive step under PCT Article 33(3) as being obvious over Kaneda et al., 5,184,282 (hereinafter Kaneda) in view of Jones, 5,928,347.

a. As to claims 18-22, Kaneda discloses some of the claim limitations, as discussed above with respect to claims 1-8, but fails to disclose the limitations regarding the transferring of digitally encoded media from an external system to a PDA to the expansion card to the removable memory and later reading, decoding, and playing back the digitally encoded media.

Jones discloses transferring digitally encoded media from an external system to a PDA to the expansion card to the removable memory and later reading, decoding, and playing back the digitally encoded media (note abstract and column 1, lines 40-50, column 2, line 39 - column 3, line 2, and column 3, line 30 - column 4, line 8).

It would have been obvious to one of ordinary skill in the art at the time of the invention to use the removable memory card to store digitally encoded media for later playback in the system of Kaneda, as Jones teaches, so as to allow the memory card to be used with consumer devices, as Jones teaches in column 3, lines 40-50 and column 4, lines 40-50.

b. As to claims 23-27, Kaneda discloses substantially all the limitations, as discussed above with respect to claims 1-8, with the exception of the data in the removable memory being address book records, telephone communications, and map information.

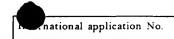
Jones teaches that the removable memory can store various kinds of data such as address book data and voice and pager messages (note column 2, lines 39-47).

It would have been obvious to one of ordinary skill in the art at the time of the invention to allow any kind of data, including address book data and voice and pager messages, in the memory of Kaneda, as Jones teaches, so as to allow the memory card to be used with a wide variety of applications and consumer products, as Jones teaches in column 2, lines 39-47.

- c. As to claims 31-34 and 36, the claimed limitations have already been discussed above with respect to claims 18-22.
- d. As to claims 35 and 37, Kaneda fails to disclose that the PDA and card transition from a first power mode to a second power mode in response to a message received over the network.

Examiner takes Official Notice that transitioning from one power mode to another in response to a message or command is well known in the art of power management.





PCT/US00/12796

Supplemental Box

(To be used when the space in any of the preceding boxes is not sufficient)

Continuation of: Boxes I - VIII

Sheet 12

It would have been obvious to one of ordinary skill in the art at the time of the invention to transition from one power mode to another in response to a message or command in the system of Kaneda so as to manage power consumption in the system.

e. As to claim 38, the claimed limitations have already been discussed with respect to claim 8 above.

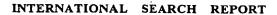
Claims 48-55 and 59-62 meet the criteria set out in PCT Article 33(2)-(4), because the prior art does not teach or fairly suggest transferring information between the first and second networks via the first-level and second-level removable expansion modules, where the first-level module is coupled to a portable host via a first-level slot and the second-level module is coupled to the first-level module via a second-level slot, or transferring information between a first-level module and a second-level module, where the first-level module is coupled to a portable host via a first-level slot and the second-level module is coupled to the first-level module via a second-level slot.

	NEW	CITATIONS	
NONE			

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
ΑZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	ТJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav	TM	Turkmenistan
BF	Burkina Faso	GR	Greece		Republic of Macedonia	TR	Turkey
BG	Bulgaria	HU	Hungary	ML	Mali	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MN	Mongolia	UA	Ukraine
BR	Brazil	IL	Israel	MR	Mauritania	UG	Uganda
BY	Belarus	IS	Iceland	MW	Malawi	US	United States of America
CA	Canada	IT	Italy	MX	Mexico	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NE	Niger	VN	Viet Nam
CG	Congo	KE	Kenya	NL	Netherlands	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NO	Norway	zw	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's	NZ	New Zealand		
CM	Cameroon		Republic of Korea	PL	Poland		
CN	China	KR	Republic of Korea	PT	Portugal		
CU	Cuba	ΚZ	Kazakstan	RO	Romania		
CZ	Czech Republic	LC	Saint Lucia	RU	Russian Federation		
DE	Germany	LI	Liechtenstein	SD	Sudan		
DK	Denmark	LK	Sri Lanka	SE	Sweden		
EE	Estonia	LR	Liberia	SG	Singapore		
	• 8					•	



Form PCT/ISA/210 (second sheet) (July 1998)*

International application No. PCT/US00/12796

A. CLASSIFICATION OF SUBJECT MATTER IPC(7): G06F 3/00, 13/00; H05K 5/00, 7/00 US CL: 710/ 2, 102; 361/684, 686 According to International Patent Classification (IPC) or to bo	th national classification and IPC					
B. FIELDS SEARCHED						
Minimum documentation searched (classification system follow	wed by classification symbols)					
U.S. : 710/1-2, 100-102; 361/679-686						
Documentation searched other than minimum documentation to	the extent that such documents are included	in the fields searched				
Electronic data base consulted during the international search	(name of data base and, where practicable	, search terms used)				
STN, WEST 2.0						
C. DOCUMENTS CONSIDERED TO BE RELEVANT						
Category* Citation of document, with indication, where	appropriate, of the relevant passages	Relevant to claim No.				
X US 5,184,282 A (KANEDA et al) 0 21-25 and col. 2, line 45 - col. 3, line		1-3, 5, 7, 9-17, 28-30, 40, 41, 49, 59, 60				
4, 6, 8, 18-27, 31-38, 42-48, 50-55, 61, 62						
X,P US 6,053,748 A (BRICAUD et al) Figure 1.	25 April 2000, abstract and	39, 56-58				
Y, P US 5,928,347 A (JONES) 27 July 19 line 39 - col. 3, line 2, and col. 3, 1		18-27, 31-38, 42- 48, 50-55, 61, 62				
		1				
X Further documents are listed in the continuation of Box						
 Special categories of cited documents: "A" document defining the general state of the art which is not considere 	"T" later document published after the int date and not in conflict with the app	lication but cited to understand				
to be of particular relevance	the principle or theory underlying th "X" document of particular relevance; the					
"E" earlier document published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is	considered novel or cannot be considered novel or cannot be considered when the document is taken alone					
cited to establish the publication date of another citation or othe special reason (as specified)	"Y" document of particular relevance; the					
O document referring to an oral disclosure, use, exhibition or other means combined with one or more other such documents, such combination being obvious to a person skilled in the art						
P document published prior to the international filing date but later tha the priority date claimed	n •&• document member of the same pater	nt family				
Date of the actual completion of the international search	Date of mailing of for International sc	arch report				
08 SEPTEMBER 2000						
Name and mailing address of the ISA/US Commissioner of Patents and Trademarks	Authorized officer					
Box PCT Washington, D.C. 20231	SUMATI LEFKOWITZ	in Loran				
Facsimile No. (703) 305-3230	Telephone No. (703) 308 3907	no Joyer				



INTERNATIONAL SEARCH REPORT

International application No. PCT/US00/12796

Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No.							
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Refevant to claim No					
Y	US 5,615,344 A (CORDER) 25 March 1997, Figures 1 and 3.	4, 6					
Y	US 5,611,055 A (KRISHAN et al) 11 March 1997, Figure 2.	8					
ļ							
	•						
χ-	*						
	•						

From the INTERNATIONAL PRELIMINARY EXAMINING AUTHORITY

TOWNSEND AND TOWNSEND AND CREW LLP TWO EMBARCADERO CENTER 8TH FLOOR SAN FRANCISCO, CA 94111-3834		WRITTEN OPINION (PCT Rule 66)		
		Date of Mailing (day/month/year)	30 APR 2001	
Applicant's or agent's file reference 20631-11PC		REPLY DUE within TWO months from the above date of mailing		
International application No.	International filing date	e (day/month/year)	Priority date (day/month/year)	
PCT/US00/12796	09 MAY 2000		11 MAY 1999	
International Patent Classification (IPC) IPC(7): G06F 3/00, 13/00; H05K 5/0	or both national classifi 00, 7/00 and US Cl.: 7	cation and IPC 10/ 2, 102; 361/684,	686	
Applicant SOCKET COMMUNICATIONS, INC.				
1. This written opinion is the first (first, etc.) drawn by this International Preliminary Examining Authority. 2. This opinion contains indications relating to the following items: I X Basis of the opinion II Priority III Non-establishment of opinion with regard to novelty, inventive step or industrial applicability IV Lack of unity of invention V X Reasoned statement under Rule 66.2(a)(ii) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement VI Certain documents cited VII Certain defects in the international application VIII Certain observations on the international application 3. The applicant is hereby invited to reply to this opinion. When? See the time limit indicated above. The applicant may, before the expiration of that time limit, request this Authority to grant an extension, rese Rule 66.2(d). How? By submitting a written reply, accompanied, where appropriate, by amendments, according to Rule 66.3. For the form and the language of the amendments, see Rules 66.8 and 66.9. Also For an additional opportunity to submit amendments, see Rules 66.8 and 66.9. For an informal communication with the examiner, see Rule 66.6. If no reply is filed, the international preliminary examination report must be established according to Rule 69.2 is: 11 SEPTEMBER 2001				
Name and mailing address of the IPEA/	LIC	Authorized officer	\cap H	
I Name and mailing address of the IPEA/	US .	I MULLIOTIZED OFFICER	4 /	

Omminimized report mess of the second	
Name and mailing address of the IPEA/US	Authorized officer Peggy Ham
Commissioner of Patents and Trademarks	1 6957 (1009

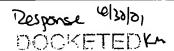
Box PCT Washington, D.C. 20231

Facsimile No. (703) 305-3230

SUMATI LEFKOWITZ

Telephone No. (703) 308-7790

Form PCT/IPEA/408 (cover sheet) (July 1998) *





ternational	application	No.
PCT/US0	0/12796	

I. Ba	sis of the opinion	
1 With	regard to the elements of the international app	plication: *
x	the international application as original	
=	the description:	
X		, as originally filed
	pages NONE	, filed with the demand
	pages NONE	, filed with the letter of
	r-b-	•
\mathbf{x}	the claims:	N. 61 1
	pages 24-34	, as originally filed
	10	, as amended (together with any statement) under Article 19
	pages NONE	ed with the letter of, filed with the demand
	pages NONE, fil	ed with the letter of
	the drawings:	
X	pages 1-24	, as originally filed
	pages NONE	, filed with the demand
	pages NONE	, filed with the letter of
	Pages	
\mathbf{x}	the sequence listing part of the description	on:
ت	NONE NONE	, as originally filed
	pages NONE	, filed with the demand
	pages NONE	, filed with the letter of
	the language of publication of the inter the language of the translation furnished fo	for the purposes of international search (under Rule 23.1(b)). mational application (under Rule 48.3(b)). r the purposes of international preliminary examination (under Rules 55.2 and/
	or 55.3).	
3. With dra	h regard to any nucleotide and/or amino a wn on the basis of the sequence listing:	cid sequence disclosed in the international application, the written opinion was
	contained in the international applicati	on in printed form.
	filed together with the international ap	
님	furnished subsequently to this Authorit	
님	furnished subsequently to this Authorit	
		ished written sequence listing does not go beyond the disclosure in the
		d in computer readable form is identical to the writen sequence listing has
4. X		cancellation of:
	X the description, pages NON	E
	X the claims, Nos. NON	E
	X the drawings, sheets/fig NON	E
5.	This opinion has been drawn as if (some o	f) the amendments had not been made, since they have been considered to go d in the Supplemental Box (Rule 70.2(c)).
		the receiving Office in response to an invitation under Article 14 are referred to



ternational application No.
PCT/US00/12796

V. Reasoned statement under Rule 66.2(a)(ii) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement				
1.	statement			
	Novelty (N)	Claims	NONE	YES
		Claims	1-62	NO
	Inventive Step (IS)	Claims	NONE	YES
		Claims	1-62	NO
		OI.	1.62	. YES
	Industrial Applicability (IA)	Claims	1-62	NO NO
		Claims	NONE	NO

2. citations and explanations

Claims 1, 28-30, 39, 40, and 56-58 lack novelty under PCT Article 33(2) as being anticipated by Kaneda et al., 5,184,282 (hereinafter Kaneda).

- a. As to claims 1, 28, 30, and 40, Kaneda discloses a removable expansion card (note Figure 9) for a portable host, comprising an expansion card frame and PCB, a host-interconnect for coupling with the host, an I/O interconnect for coupling with the external I/O device (note Figure 9, element 27), I/O adapter circuitry for the I/O device (inherent in the serial to parallel conversion required to interface a telephone device with a computer), a slot for a removable memory (note Figure 9, elements 16 and 17 and column 5, lines 35-40), and removable memory adapter circuitry for the removable memory (inherent in the fact that the removable memory and host computer communicate) (note abstract, column 1, lines 12-25, and column 2, line 45 column 3, line 4).
- b. As to claim 29, Kaneda discloses that the removable memory is a private memory for application-specific circuitry and that the management of the removable memory is an ancillary function to the primary function of the specific application (note column 1, lines 12-34).
- c. As to claims 39, 56, 57, and 58, Kaneda discloses a slot assembly for a removable expansion memory comprising a PCB, an I/O connector mounted on the PCB providing a first partial bottom of slot, a guide/connector assembly mounted on PCB having connector fingers and providing a second partial bottom of the slot, rear sides of the slot, and slot back stop, upper outside frame of expansion module frame providing front sides of slot and the lid of the expansion module providing the top of the slot (note Figures 6 and 9).

Claims 2, 3, 5, 7, 9-17, 41, 42 lack an inventive step under PCT Article 33(3) as being obvious over Kaneda et al., 5,184,282 (hereinafter Kaneda).

a. As to claim 2, Kaneda fails to disclose that the card is a CompactFlash card.

Examiner takes Official Notice that CompactFlash cards are well known in the art of removable expansion cards.

(Continued on Supplemental Sheet.)



Supplemental Box

(To be used when the space in any of the preceding boxes is not sufficient)

Continuation of: Boxes I - VIII

Sheet 10

TIME LIMIT:

The time limit set for response to a Written Opinion may not be extended. 37 CFR 1.484(d). Any response received after the expiration of the time limit set in the Written Opinion will not be considered in preparing the International Preliminary Examination Report.

V. 2. REASONED STATEMENTS - CITATIONS AND EXPLANATIONS (Continued):

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a CompactFlash in the system of Kaneda so as to allow the system to be compatible with a widely used standard and to allow the system to take advantage of the many benefits provided by a CompactFlash card.

b. As to claim 3, Kaneda fails to discloses that the removable memory slot is compatible with a MMC, and the removable memory adapter circuitry is MMC adapter circuitry.

Examiner takes Official Notice that MMC is well known in the art of removable expansion cards.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of MMC components and circuitry in the system of Kaneda so as to allow the system to be compatible with a widely used standard and to allow the system to take advantage of the many benefits provided by MMC.

c. As to claim 5, Kaneda fails to disclose that the I/O adapter circuitry is a LAN adapter and the I/O interconnect includes a cable having a standard LAN connector.

Examiner takes Official Notice that LAN cards, LAN cables and LAN connectors are well known in the art of

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a LAN card, LAN cable and LAN connector in the system of Kaneda so as to allow the system of Kaneda to function in a network environment.

d. As to claim 7, Kaneda fails to disclose that the I/O interconnect is a Honda-style 15-pin connector integral to the card.

Examiner takes Official Notice that Honda-style 15-pin connectors are well known in the art of expansion cards. It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of Honda-style 15-pin connectors in the system of Kaneda so as to be compatible with a widely used connector and take advantage of the benefits provided by Honda-style 15-pin connectors.

e. As to claims 9-17, Kaneda discloses some of the claim limitations, as discussed above with respect to claims 1-8, but fails to disclose the environmental limitations of video and audio digitally encoded data.

Examiner takes Official Notice that the processing of video and audio data in the manner recited in the claim limitations is well known in the art of multimedia processing.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of any kind of digitally encoded data, including digitally encoded video and audio data, as the data stored in the removable memory since the type of data stored and processed would not change the overall configuration of an expansion card with a slot for a removable memory and a connector for an I/O device.

f. As to claim 41, the claimed elements have already been discussed above with respect to claim 1 above, with the exception of a second-level I/O interface for coupling with a second external I/O device, and second-level I/O adapter circuitry for the second I/O device.

Having a second-level I/O interface for coupling with a second external I/O device, and second-level I/O adapter circuitry for the second I/O device is simply a duplication of the first I/O interface, external I/O device and I/O adapter. And according to In 1. Harza, 274 F.2d 669, 124 USPQ 378 (CCPA 1960), mere duplication of parts has no patentible significance unless a new and unexpected result is produced, so that it would have been obvious to one of ordinary skill in the art at the time of the invention to have a second-level I/O interface for coupling with a second external I/O device, and second-level I/O adapter circuitry for the second I/O device.

g. As to claim 42, Kaneda discloses that the first external I/O device is a phone (note column 4, lines 60-62).

Claims 4 and 6 lack an inventive step under PCT Article 33(3) as being obvious over Kaneda et al., 5,184,282 (hereinafter Kaneda) in view of Corder, 5,615,344.

a. As to claim 4, Kaneda fails to disclose that the I/O adapter circuitry is a serial I/O adapter and the I/O interconnect includes a cable having a standard serial connector.

Corder discloses a card with a serial I/O adapter and an I/O interconnect that includes a cable having a serial connector (note Figures 1 and 3).

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a serial I/O adapter and serial connector so as to interface with the wide variety of serial I/O devices available in the art.

b. As to claim 6, Kaneda fails to disclose that the I/O adapter circuitry is a parallel adapter and the I/O



Supplemental Box

(To be used when the space in any of the preceding boxes is not sufficient)

Continuation of: Boxes I - VIII

Sheet 11

interconnect includes a cable having a standard parallel connector.

Corder discloses a card with a parallel I/O adapter and an I/O interconnect that includes a cable having a parallel connector (note Figure 1).

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a serial I/O adapter and serial connector so as to interface with the wide variety of parallel I/O devices available in the art.

Claims 8 and 43-48 lack an inventive step under PCT Article 33(3) as being obvious over Kaneda et al., 5,184,282 (hereinaster Kaneda) in view of Krishan et al., 5,611,055 (hereinaster Krishan).

a. As to claim 8, Kaneda fails to disclose that the card is designed to abut and fasten with at least part of the I/O device such that the I/O interconnect for coupling with the I/O device is cableless.

Krishan discloses that a card is designed to abut and fasten with at least part of the I/O device such that the I/O interconnect for coupling with the I/O device is cableless.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of a card that is designed to abut and fasten with at least part of the I/O device such that the I/O interconnect for coupling with the I/O device is cableless in the system of Kaneda, as Krishan teaches, so as to avoid problems associated with cables, such as tangling with other cables and cluttering and not having cables of sufficient length.

b. As to claim 43, Kaneda fails to disclose that the second-level I/O adapter circuitry includes wireless interace circuitry, the second-level I/O interface includes a wireless transducer, and the second-level I/O device includes wireless network interface circuitry, and the coupling to the second external I/O device is wireless, but does disclose that the connector 27 allows communication devices such as networks to be coupled to card.

Krishan discloses that the I/O interconnect for coupling with the I/O device is cableless.

It would have been obvious to one of ordinary skill in the art at the time of the invention to couple a wireless network, with its associated transducer, to the card to allow a network to be coupled to the system without dealing with problems associated with cables, such as tangling with other cables and cluttering and not having cables of sufficient length.

c. As to claim 44, Kaneda fails to disclose that the first wireless network interface circuitry is RF wireless network interface circuitry and the wireless transducer includes an antenna.

Examiner takes Official Notice that RF wireless network interface circuitry and antennas are well known in the art of wireless communications.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of RF wireless network interface circuitry and antennas in the system of Kaneda, since RF wireless network interface circuitry and antennas are reliable and cost-effective means for carrying out communication over a wireless network.

d. As to claim 45, Kaneda fails to disclose that the RF wireless network interface circuitry is compatible with the Bluetooth wireless network standard.

The Bluetooth wireless network standard is a well known standard in the art of wireless communications.

It would have been obvious to one of ordinary skill in the art at the time of the invention to have the RF wireless network interface circuitry be compatible with the Bluetooth wireless network standard so as to allow the RF wireless network interface circuitry to be in conformance with Bluetooth, which is among the latest developments in wireless communications, thereby allowing the system of Kaneda to be implemented in the many systems which use the Bluetooth standard.

Claims 18-27 and 31-38 lack an inventive step under PCT Article 33(3) as being obvious over Kaneda et al., 5,184,282 (hereinafter Kaneda) in view of Jones, 5,928,347.

a. As to claims 18-22, Kaneda discloses some of the claim limitations, as discussed above with respect to claims 1-8, but fails to disclose the limitations regarding the transferring of digitally encoded media from an external system to a PDA to the expansion card to the removable memory and later reading, decoding, and plaving back the digitally encoded media.

Jones discloses transferring digitally encoded media from an external system to a PDA to the expansion card to the removable memory and later reading, decoding, and playing back the digitally encoded media (note abstract and column 1, lines 40-50, column 2, line 39 - column 3, line 2, and column 3, line 30 - column 4, line 8).

It would have been obvious to one of ordinary skill in the art at the time of the invention to use the removable memory card to store digitally encoded media for later playback in the system of Kaneda, as Jones teaches, so as to allow the memory card to be used with consumer devices, as Jones teaches in column 3, lines 40-50 and column 4, lines 40-50.

b. As to claims 23-27, Kaneda discloses substantially all the limitations, as discussed above with respect to claims 1-8, with the exception of the data in the removable memory being address book records, telephone communications, and map information.

Jones teaches that the removable memory can store various kinds of data such as address book data and voice and pager messages (note column 2, lines 39-47).

It would have been obvious to one of ordinary skill in the art at the time of the invention to allow any kind of data, including address book data and voice and pager messages, in the memory of Kaneda, as Jones teaches, so as to allow the



ernational application No.
PCT/US00/12796

Supplemental Box

(To be used when the space in any of the preceding boxes is not sufficient)

Continuation of: Boxes I - VIII

Sheet 12

memory card to be used with a wide variety of applications and consumer products, as Jones teaches in column 2, lines 39-47.

- c. As to claims 31-34 and 36, the claimed limitations have already been discussed above with respect to claims 18-22.
- d. As to claims 35 and 37, Kaneda fails to disclose that the PDA and card transition from a first power mode to a second power mode in response to a message received over the network.

Examiner takes Official Notice that transitioning from one power mode to another in response to a message or command is well known in the art of power management.

It would have been obvious to one of ordinary skill in the art at the time of the invention to transition from one power mode to another in response to a message or command in the system of Kaneda so as to manage power consumption in the system.

e. As to claim 38, the claimed limitations have already been discussed with respect to claim 8 above.

Claims 48-55 and 59-62 meet the criteria set out in PCT Article 33(2)-(4), because the prior art does not teach or fairly suggest transferring information between the first and second networks via the first-level and second-level removable expansion modules, where the first-level module is coupled to a portable host via a first-level slot and the second-level module is coupled to the first-level module via a second-level slot, or transferring information between a first-level module and a second-level module, where the first-level module is coupled to a portable host via a first-level slot and the second-level module is coupled to the first-level module via a second-level slot.

	NEW	CITATIONS	
NONE		•	